



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENTS

In re Application of:

Hideyuki Andoh

**BEST AVAILABLE COPY**

Serial No. 10/013,087

Filed: December 7, 2001

For: **SEMICONDUCTOR DEVICES AND THE  
MANUFACTURING METHOD OF THE SAME**

Art Unit: 2813

Examiner: Schillinger, Laura M

# 8 / Election  
12-23-02  
✓

**FIRST RESPONSE**

Assistant Commissioner for Patents  
Washington, DC 20231

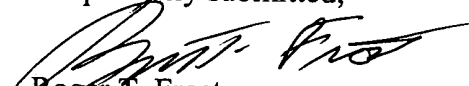
Sir:

Responsive to the Office Action dated November 13, 2002 in the patent application identified above, the Applicant here elects Invention Group I, Claims 1-9, drawn to a semiconductor device, for further prosecution in this application.

This election is without traverse.

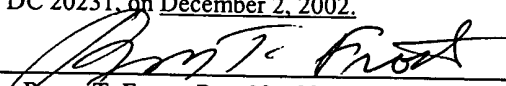
The foregoing is submitted as a complete response to the Office Action identified above. The applicant awaits further examination consistent with the election herein.

Respectfully submitted,

  
Roger T. Frost  
Reg. No. 22,176

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Atlanta, Georgia 30309-4530  
Our Docket: 44471-267680 (13700)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231, on December 2, 2002.

  
Roger T. Frost - Reg. No. 22,176